

Docket No.: P-180

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Jang Geun OH and Sang Ho LEE :

Serial No.: New U.S. Application :

Confirm. No.: :

Filed: May 8, 2001 :

For: METHOD AND APPARATUS FOR ADJUSTING CLOCK
THROTTLE RATE BASED ON USAGE OF CPU

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D. C. 20231

Sir:

Prior to initial examination on the merits, please amend the above-identified application as follows:

IN THE SPECIFICATION:

Please amend the specification by replacing paragraphs as follows:

A. Specification Paragraphs With Mark-ups to Show Changes Made

The following are mark-ups to show changes made to paragraph [3] starting at page 1, line 12 and ending at page 2, line 4:

[3] A power management method in the related art will be described in conjunction with FIG. 1 which illustrates a related method for managing the electric power consumed in a system. Once the system is turned on (Step S10), the system 4 determines whether a signal has been inputted from an external input device, such as a keyboard or mouse, to the system for a predetermined period of time (Step S11). If there is a signal inputted from the external input device within a predetermined period of time, the system is maintained in its ON state. On the other hand, if no signal is inputted from the external input device for the predetermined period of time, the system is switched from its ON state to an idle mode (Step S12) to reduce the [clock speed of a CPU, which is part of the system, and thus reduce the electric power consumed in the system as the clock throttle rate of the CPU is reduced by 50% and reduces the consumption of electric power] electric power consumed in the system.

The following are mark-ups to show changes made to paragraph [4] starting at page 2, line 5 and ending at page 2, line 10:

[4] In systems where an advanced power management (APM) is applied, the operational mode when no signal is inputted from the external input device for a certain period of time is referred to as an "idle mode" or "doze mode". When the system is

switched to the "idle mode", the basic input/output system (BIOS) of the system conducts an operation for reducing the clock speed supplied to the CPU and decreasing LCD brightness, etc., thereby reducing the consumption of electric power.

B. Clean Specification Changes

Please replace paragraph [3] starting at page 1, line 12 and ending at page 2, line 4:

[3] A power management method in the related art will be described in conjunction with FIG. 1 which illustrates a related method for managing the electric power consumed in a system. Once the system is turned on (Step S10), the system 4 determines whether a signal has been inputted from an external input device, such as a keyboard or mouse, to the system for a predetermined period of time (Step S11). If there is a signal inputted from the external input device within a predetermined period of time, the system is maintained in its ON state. On the other hand, if no signal is inputted from the external input device for the predetermined period of time, the system is switched from its ON state to an idle mode (Step S12) to reduce the electric power consumed in the system.

Please replace paragraph [4] starting at page 2, line 5 and ending at page 2, line 10:

[4] In systems where an advanced power management (APM) is applied, the operational mode when no signal is inputted from the external input device for a certain

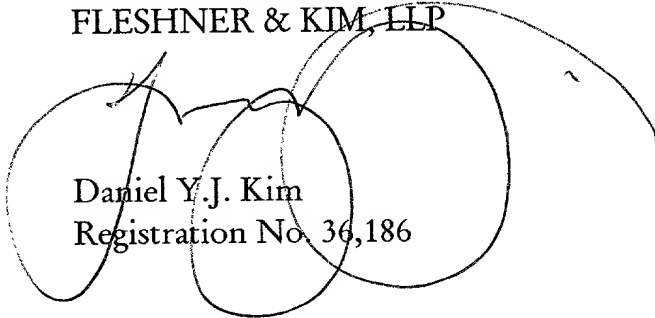
period of time is referred to as an "idle mode" or "doze mode". When the system is switched to the "idle mode", the basic input/output system (BIOS) of the system conducts an operation for reducing the clock speed supplied to the CPU and decreasing LCD brightness, etc., thereby reducing the consumption of electric power.

REMARKS

Claims 1-18 are pending. The specification has been amended for clarification.

Prompt examination and allowance in due course are respectfully solicited.

Respectfully submitted,
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